

## EAST Search History

| Ref # | Hits | Search Query                                                                                                                                           | DBs                                                     | Default Operator | Plurals | Time Stamp          |
|-------|------|--------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------|------------------|---------|---------------------|
| L1    | 43   | ((general ADJ purpose ADJ register\$1 OR GPR\$1) near5 (secur\$3)) AND (secur\$3 near3 memory)                                                         | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2008/04/11<br>14:46 |
| L2    | 55   | ((((general near2 register \$1) OR GPR\$1) WITH ((secur \$3) near5 (bit \$1 OR flag\$4 OR indicat\$3 OR tag\$4 OR mark\$3 OR identif\$4 OR value\$1))) | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2008/04/11<br>14:46 |
| L3    | 676  | ((((register\$1)) near5 ((secur \$3) near3 (bit \$1 OR flag\$4 OR indicat\$3 OR tag\$4 OR mark\$3 OR identif\$4 OR value\$1)))                         | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2008/04/11<br>14:47 |
| L4    | 1778 | (711/163).<br>OCLS.                                                                                                                                    | US-PGPUB;<br>USPAT; USOCR                               | OR               | OFF     | 2008/04/11<br>14:47 |
| L5    | 34   | L3 AND L4                                                                                                                                              | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2008/04/11<br>14:47 |

|    |    |                                                                                                                                                                                                                                                |                                             |    |    |                  |
|----|----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------|----|----|------------------|
| L6 | 21 | ((instruct\$3 OR command\$3) near5 (fetch\$3 OR load\$3)) WITH (determin\$3 OR indicat\$3 OR establish\$3) WITH ((secur\$3 OR protect\$3 OR restrict\$3 OR prohibit\$3) near3 (memory OR RAM OR SRAM OR SDRAM OR DRAM OR ROM)) WITH address\$3 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/04/11 14:47 |
| L7 | 18 | ((secur\$3 OR protect\$3 OR restrict\$3 OR prohibit\$3) near3 (status OR flag\$1 OR indicator\$1)) WITH (general ADJ purpose ADJ register\$1 OR GPR\$1 OR general ADJ register\$1)                                                             | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/04/11 14:48 |
| L8 | 73 | ((secur\$3 OR protect\$3 OR restrict\$3 OR prohibit\$3) near5 (status OR flag\$1 OR indicator\$1)) SAME (general ADJ purpose ADJ register\$1 OR GPR\$1 OR general ADJ register\$1)                                                             | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/04/11 14:48 |

|     |      |                                                                                                                                                                                                                                              |                                                         |    |     |                     |
|-----|------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------|----|-----|---------------------|
| L9  | 12   | (context ADJ switch\$3)<br>SAME stack\$1<br>SAME (RAM OR SDRAM OR SRAM OR DRAM) SAME interrupt\$1                                                                                                                                            | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2008/04/11<br>14:49 |
| L10 | 407  | ((secur\$3 OR protect\$3 OR restrict\$3 OR prohibit\$3)<br>near3 (memory OR RAM OR SRAM OR SDRAM OR DRAM)) WITH<br>((debug\$4 OR program\$4)<br>near3 (key\$1 OR token\$1 OR passcode\$1 OR pin\$1 OR password\$1))                          | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2008/04/11<br>14:49 |
| L11 | 1778 | (711/163).<br>OCLS.                                                                                                                                                                                                                          | US-PGPUB;<br>USPAT; USOCR                               | OR | OFF | 2008/04/11<br>14:49 |
| L12 | 6    | L10 AND L11                                                                                                                                                                                                                                  | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2008/04/11<br>14:49 |
| L13 | 42   | ((secur\$3 OR protect\$3 OR restrict\$3 OR prohibit\$3)<br>near3 (memory OR RAM OR SRAM OR SDRAM OR DRAM)) SAME<br>((debug\$4 OR (error near3 (correct\$3 OR fix\$3))) WITH<br>(key\$1 OR token\$1 OR passcode\$1 OR pin\$1 OR password\$1)) | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2008/04/11<br>14:49 |

|     |    |                                                                                                                                                                                                      |                                                         |    |    |                     |
|-----|----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------|----|----|---------------------|
| L14 | 20 | ((secur\$3 OR protect\$3 OR restrict\$3 OR prohibit\$3) near3 (status OR flag\$1 OR indicat\$3)) WITH (general ADJ purpose ADJ register\$1 OR GPRS\$1 OR general ADJ register\$1)                    | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/04/11<br>14:51 |
| L15 | 1  | (arithmetic near5 (operation\$1 OR instruction \$1)) SAME (((status ADJ register\$1)) WITH ((secur \$3 OR protect \$3 OR restrict \$3 OR prohibit \$3) near5 (requir\$3 OR enabl\$3 OR indicat\$3))) | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/04/11<br>14:52 |
| L16 | 1  | ((((status ADJ register\$1) near5 (unchang \$3)) WITH ((secur\$3 OR protect\$3 OR restrict\$3 OR prohibit\$3) near5 (requir\$3 OR enabl\$3 OR indicat\$3)))                                          | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/04/11<br>14:52 |

|     |    |                                                                                                                                                                                                                                                                              |                                             |    |    |                  |
|-----|----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------|----|----|------------------|
| L17 | 1  | ((((status ADJ register\$1) near5 (unchang \$3 OR original \$2 ADJ state\$1 OR uniform OR unvar\$4 OR constant OR unalter\$4 OR consistent\$2 OR changeless \$2)) SAME ((secur\$3 OR protect\$3 OR restrict\$3 OR prohibit\$3) near5 (requir\$3 OR enabl\$3 OR indicat\$3))) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/04/11 14:52 |
| L18 | 24 | ((((register\$1) near5 (unchang \$3 OR original \$2 ADJ state\$1 OR uniform OR unvar\$4 OR constant OR unalter\$4 OR consistent\$2 OR changeless \$2)) SAME ((secur\$3 OR protect\$3 OR restrict\$3 OR prohibit\$3) near5 (requir\$3 OR enabl\$3 OR indicat\$3)))            | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/04/11 14:52 |
| L19 | 36 | ((((register\$1) near5 (unchang \$3 OR original \$2 ADJ state\$1 OR uniform OR unvar\$4 OR constant OR unalter\$4 OR consistent\$2 OR changeless \$2)) SAME ((secur\$3 OR protect\$3 OR                                                                                      | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2008/04/11 14:52 |

|     |   |                                                                                                                                                                                                                                                                                  |                                                         |    |    |                     |
|-----|---|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------|----|----|---------------------|
|     |   | restrict\$3 OR<br>prohibit\$3<br>WITH (requir\$3<br>OR enabl\$3 OR<br>indicat\$3)))                                                                                                                                                                                              |                                                         |    |    |                     |
| L20 | 3 | ((secur\$3 OR<br>protect\$3 OR<br>restrict\$3 OR<br>prohibit\$3)<br>near3 (memory<br>OR RAM OR<br>SRAM OR<br>SDRAM OR<br>DRAM)) WITH<br>((invalid\$6 OR<br>corrupt\$3 OR<br>void\$1) near5<br>(secur\$3 OR<br>protect\$3 OR<br>restrict\$3 OR<br>prohibit\$3)<br>near5 (status)) | US-PGPUB;<br>USPAT; EPO;<br>JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2008/04/11<br>14:53 |

4/ 11/ 2008 2:54:13 PM

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